

Spring 2006

CEG 453/653-01: Design of Computing Systems

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CEG 453/653 DESIGN OF COMPUTING SYSTEMS

Spring 2006, 4:10-5:25 PM, Mon., Wed., at 153 RC

Instructor: Jack Jean

Office Hours: 3-4 PM, M, Tue, W, Thr.; 334 RC, 775-5106, jack.jean@wright.edu

Textbooks:

[H] MC 68HC11: An Introduction (2nd edition), Han-Way Huang, Delmar, 2001.

[B] Advanced PC Architecture (1st edition), William Buchanan and Austin Wilson, Addison-Wesley, 2001

Weekly Schedule:

Week	Contents	Chapters to Read
1	Overview, Lab Preparation	[H] 1, 4
2	Operation Modes, Interrupts	[H] 5, 6
3	Parallel I/O, Timer	[H] 7, 8
4	Memory Expansion and Bus Cycles	[H] 5
5	MIDTERM ; SCI and SPI	[H] 9, 10
6	6811 ADC, PC Basics	[H] 11, [B] 1, 2, 3, 4
7	Cache and Bus Control	[B] 5, 6
8	Memory	[B] 7, 12
9	Pentium Instruction Execution	[B] 10, 13, 14
10	PC Buses and I/O Ports	[B] 17, 18, 22, 24

Grading: Final letter grade: 90+ (A), 80+ (B), 70+ (C), 60+ (D), otherwise (F).

- Lab. - 30%. **You must attain at least 60% in Laboratory to pass this course.**
- HW - 10%
- Quiz - 10%. Unannounced quizzes (closed book and notes) will be given at the beginning of classes.
- Midterm - 25%; April 24, Mon.; open book and notes.
- Final - 25%; June 5, Mon., 5:45-7:45PM; Not comprehensive, open book and notes.
- Students taking CEG653 will be assigned more analysis works for assignments/tests.