Fall 2006

CEG 453/653: Design of Computing Systems

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CEG 453/653 DESIGN OF COMPUTING SYSTEMS

Fall 2006, 2:45-4 PM, Mon., Wed., at 204 Fawcett

Instructor: Jack Jean

Office Hours: 1:40-2:40 PM, M, W; 1:10-2:10 Tue., Thr.; 334 RC, 775-5106, jack.jean@wright.edu

Textbook: **MC 68HC11: An Introduction** (2nd edition), Han-Way Huang, Delmar, 2001.

Reference: **Advanced PC Architecture** (1st edition), William Buchanan and Austin Wilson, Addison-Wesley, 2001

Weekly Schedule:

Week	Contents	Materials to Read
1	Overview, Lab Preparation	Chap. 1, Chap. 4
2	Operation Modes and Memory Expansion	Chap. 5
3	Timer and Basic I/O	Sec. 7.2, 8.3, 8.6
4	Interrupts	Chap. 6
5	**MIDTERM**; Parallel I/O	Chap. 7
6	More Timer Functions	Chap. 8
7	SCI and SPI	Chap. 9, Chap. 10
8	6811 ADC	Chap. 11
9	Real Time Operating Systems	Class Notes
10	PC Architecture	Class Notes

Grading: Final letter grade: 90+ (A), 80+ (B), 70+ (C), 60+ (D), otherwise (F).

- Lab. - 30%. ***You must attain at least 60% in Laboratory to pass this course.***
- HW - 10%
- Quiz - 10%. Unannounced quizzes (closed book and notes) will be given at the beginning of classes.
- Midterm - 25%; Oct. 4, Wed.; open book and notes.
- Final - 25%; Nov. 15, Wed., 3:15-5:15PM; Not comprehensive, open book and notes.
- Students taking CEG653 will be assigned more analysis works for assignments/tests.