Winter 2010

CEG 320/520: Computer Organization

Nikolaos Bourbakis

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CEG 320/520 – COMPUTER ORGANIZATION 
WINTER 2010

Instructor: Prof. Nikolaos Bourbakis
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775-5138
nikolaos.bourbakis@wright.edu

Time/Place: Tuesday/Thursday, 2:15 – 3:30 pm, Room: 150 Russ Engr. Building.

Office Hours: Tue/Thu, 1:00 – 2:00 pm, or by appointment.


Tentative Lecture Schedule:

<table>
<thead>
<tr>
<th>Week</th>
<th>Topics</th>
<th>Reading</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Introduction, representing data</td>
<td>Chs. 1 &amp; 2</td>
</tr>
<tr>
<td>2</td>
<td>Digital logic, the Von Neumann model</td>
<td>Chs. 3 &amp; 4</td>
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<tr>
<td>3</td>
<td>The LC-3 machine &amp; simulator</td>
<td>Ch. 5</td>
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<tr>
<td>4</td>
<td>Assembly language programming</td>
<td>Chs. 6 &amp; 7</td>
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<td>5</td>
<td>Low level I/O</td>
<td>Ch. 8</td>
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<td>6</td>
<td>Midterm Exam Feb 10, 2011</td>
<td>—</td>
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<tr>
<td>7</td>
<td>Traps and Subroutines</td>
<td>Ch. 9</td>
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<tr>
<td>8</td>
<td>The stack, interrupts, and the C language</td>
<td>Chs. 10 &amp; 11</td>
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<tr>
<td>9</td>
<td>Functions and recursion</td>
<td>Chs. 14 &amp; 17</td>
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<tr>
<td>10</td>
<td>Memory and cache</td>
<td>Notes</td>
</tr>
<tr>
<td></td>
<td>Dynamic memory, high-level I/O, review</td>
<td>Chs. 18 &amp; 19</td>
</tr>
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</table>

Grading:

Midterm Exam = 30%
Final Exam = 35%
Programming Assignments (4) = 35%

90 – 100 = A; 80 – 89.9 = B;
70 – 79.9 = C; 60 – 69.9 = D;
< 60 = F

I may curve the final letter grades based on the overall distribution of scores.

Email the Class: I will send you emails regarding the class (announcements, assignments, and other important information). Thus you can respond to them and interact with me via email, if necessary.
I. Late Assignments

PROGRAMMING ASSIGNMENTS (LABS) are due by 11:55 pm on the due date. Late programming assignments will be accepted, but 10% of the total available points will be deducted for each day late. Labs are considered one day late after 11:55 pm on the due date. At 11:55 pm of each successive day (including weekends) the lab is considered an additional day late until turned in. No points will be awarded for labs turned in more than one week (7 days) late.

II. Academic Integrity

Discussion of course contents with other students is an important part of the academic process and is encouraged. However, it is expected that course programming assignments, homework assignments, and other course assignments will be completed on an individual basis. Students may discuss general concepts with one another, but may not, under any circumstances, work together on the actual implementation of any course assignment. If you work with other students on “general concepts” be certain to acknowledge the collaboration and its extent in the assignment. Unacknowledged collaboration will be considered dishonest. “Code sharing” (including code from previous quarters) is strictly disallowed. “Copying” or significant collaboration on any graded assignments will be considered a violation of the university guidelines for academic honesty. If the same work is turned in by two or more students, all parties involved will be held equally accountable for violation of academic integrity. You are responsible for ensuring that other students do not have access to your work: do not give another student access to your account, do not leave printouts in the recycling bin, pick up your printouts promptly, do not leave your workstation unattended, etc. If you suspect that your work has been compromised notify me immediately. NOTE: Failure to attend the first day of class, during which time I will explain these academic honesty policies in detail, does not excuse you from following these policies. If you have any questions about collaboration or any other issues related to academic integrity, please see me immediately for clarification.

III. Other notes

Students with disabilities or any additional needs are encouraged to set up an appointment at their convenience to discuss any classroom accommodations that may be necessary.